L Number	Hits	Search Text	DB	Time stamp
-	7	revision\$4 same rom same ((chip with computer) microprocessor) same firmware	USPAT; US-PGPUB; EPO; JPO;	2003/04/04 09:09
-	19	revision\$4 same ((chip with computer) microprocessor) same firmware	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/04/03 09:10
-	12	(revision\$4 same ((chip with computer) microprocessor) same firmware) not (revision\$4 same rom same ((chip with computer) microprocessor) same firmware)	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/04/03 09:13
-	111	firmware with patch\$3	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/04/03 09:14
-	4	(firmware with patch\$3) same interrupt	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/04/03 09:17
-	6	(firmware with patch\$3) and (revision\$4 same interrupt)	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/04/03 09:18
-	603	vector same temporary same stor\$3	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/04/04 09:10
-	43	vector same temporary same stor\$3 same interrupt	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/04/04 09:10
-	9	(interrupt adj controller) same vector same temporary	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/04/04 09:19
-	482	(interrupt adj controller) same vector	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/04/04 09:20
-	15	((interrupt adj controller) same vector).ti.	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/04/04 09:21
-	163	(vector adj table) same (vector with register)	DERWENT; IBM_TDB USPAT; US-PGPUB;	2003/04/04 09:51

0	(vector adj table) same (vector with register) same (temporary with sav\$3)	USPAT;	2003/04/04
		US-PGPUB;	09:52
		EPO; JPO;	
17	(vector adi table) same (vector with marity)		
- /	same (temporary say#2)		2003/04/04
	same (temporary savas)		09:52
		. ,	
	17		EPO; JPO; DERWENT; IBM_TDB (vector adj table) same (vector with register) USPAT: